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Dkt. 2271/60102

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of : Takashi TAKAHASHI

Serial No. : 09/408,437 Group Art Unit: 2651

Date Filed : September 29, 1999 Examiner: N. Hindi

For : SEMICONDUCTOR DEVICE WITH SATURABLE ABSORBING LAYER

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New York, N.Y. 10036

Assistant Commissioner for Patents
Washington, D.C. 20231

Technology Center 2600

RESPONSE TO CLAIM RESTRICTION REQUIREMENT

In response to the Office Action dated December 12, 2001 requiring restriction of inventions under 35 U.S.C. 121, applicant hereby elects, with traverse, to prosecute Group I, claims 1-6 and 13-18.

The restriction requirement is traversed because it is believed that all of the claimed inventions are sufficiently related to each other to be examined in one patent application.

Respectfully submitted,

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I hereby certify that this paper is being deposited this date with the U.S. Postal Service as first class mail addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Richard F. Jaworski Date
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